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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/994,293	
	Filing Date	November 26, 2001	
	First Named Inventor	Abdalla Aly Naem	
	Group Art Unit	2826	
	Examiner Name	Unknown	
Total Number of Pages in This Submission	18	Attorney Docket Number	072219-0278251 (P04976-P01)

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment <input type="checkbox"/> After Final (Response) <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement, Form PTO-1449 and 2 references <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <div>Remarks</div>	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below) Return Receipt Postcard Certificate of Mailing Deposit Account Authorization

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Abdalla Aly Naem

Application No. 09/994,293

Filed: November 26, 2001

For: BIPOLAR TRANSISTOR WITH AN
ULTRA SMALL SELF-ALIGNED
POLYSILICON EMITTER AND
METHOD OF FORMING THE
SILICON GERMANIUM BASE OF
THE TRANSISTOR

) Group Art Unit: 2826

) Examiner: Unknown

) INFORMATION DISCLOSURE
) STATEMENT

) 50 Fremont Street, 5th Floor

) San Francisco, CA 94105-2230

) Customer No.: 27271

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant submits herewith patents, publications or other information [attached hereto and listed on the attached Form PTO-1449 (modified)] of which he is aware, which he believes may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

This Information Disclosure Statement:

- (a) ☐ accompanies the new patent application submitted herewith. 37 CFR § 1.97(a).
- (b) ☐ is filed within three months after the filing date of the application or within three months after the date of entry of the national stage of a PCT application as set forth in 37 CFR § 1.491.
- (c) ☒ as far as is known to the undersigned, is filed before the mailing date of a first Office Action on the merits.

CERTIFICATE OF MAILING

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Dated: APR 22 2002 By: [Signature]

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- (d) ☐ is filed after the first office action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned, prior to the mailing date of either a final rejection or a notice of allowance, whichever occurs first, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below.
- (e) ☐ is filed after the mailing date of either a final rejection or a notice of allowance, whichever occurred first, and is accompanied by the fee (\$180) set forth in 37 CFR § 1.17(i)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

[If either of boxes (d) or (e) are checked above, the following "certification" under 37 CFR § 1.97(e) may need to be completed.] The undersigned certifies that:

- (f) ☐ Each item of information contained in the information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.
- (g) ☐ No item of information contained in this information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this information disclosure statement.

A list of the patent(s) or publication(s) is set forth on the attached Form PTO-1449 (Modified).

A copy of the items on PTO-1449 (Modified) is supplied herewith:

- (h) ☒ each (i) ☐ none (j) ☐ only those listed below:

Those patent(s) or publication(s) which are marked with an asterisk (*) in the attached form PTO-1449 (Modified) are not supplied because they were previously cited by or submitted to the Office in a prior application no. _____, filed _____ and relied upon in this application for an earlier filing date under 35 U.S.C. § 120.

A concise explanation of relevance of the items listed on form PTO-1449 (Modified) is:

- (k) ☒ not given
- (l) ☐ given for each listed item

- (m) ☐ given for only non-English language listed item(s) [Required]
- (n) ☐ is in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to the relevant portions of the references [copy attached].

The Examiner is reminded that a "concise explanation of the relevance" of the submitted items "may be nothing more than identification of the particular figure or paragraph of the patent or publication which has some relation to the claimed invention," MPEP § 609.

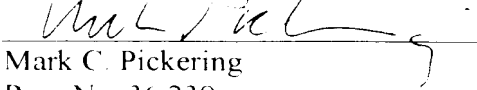
While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the listed references.

The Commissioner is hereby authorized to charge our Deposit Account No. 03-3975 for any fees required in connection with the filing of this Information Disclosure Statement. **A duplicate copy of this Notice is enclosed for this purpose.** In particular, in the event that an Office Action has crossed in the mail with this Information Disclosure Statement, the Commissioner is authorized to charge the above-named deposit account for any fees required pursuant to CFR § 1.17(p) or 1.17 (i)(1).

Respectfully submitted,
PILLSBURY WINTHROP LLP

Dated: 4-12-02

By: 
Mark C. Pickering
Reg. No. 36,239

Attorney for Assignee

Atty. Docket No: 072219-0278251 [P04976-P01]

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Application No.

09/994.293

[P04976-P011]

Applicant(s)
Abdalla Aly Naem

Filing Date

November 26, 2001

Group Art Unit

2826

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

C. A. King et al., "Very Low Cost Graded SiGe Base Bipolar Transistors for a High-Performance Modular BiCMOS Process", IEDM, 1999, pps. 565-568.

Wim van der Wel et al., "Poly-Ridge Emitter Transistor (PRET): Simple Low-Power Option to a Bipolar Process", IEDM, 1993, pps. 453-456.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.